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04743 7590 08/05/2005

**MARSHALL, GERSTEIN & BORUN LLP
233 S. WACKER DRIVE, SUITE 6300
SEARS TOWER
CHICAGO, IL 60606**

09/02/2005 CNGUYEN1 00000070 10615478

01 FC:1501 1400.00 OP
02 FC:1504 300.00 OP
03 FC:0001 APPLICATION NO. FILING DATE

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James P. Zeller	(Depositor's name)
<i>[Signature]</i>	(Signature)
August 30, 2005	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/615,478	07/08/2003	Kwan Yong Lim	29936/39485	9262

TITLE OF INVENTION: METHOD OF FORMING GATE IN SEMICONDUCTOR DEVICE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	11/07/2005
EXAMINER		ART UNIT	CLASS- SUBCLASS		
SARKAR, ASOK K		2891	438-585000		

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

2. For printing on the patent front page, list
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1 Marshall, Gerstein &
 2 Borun LLP
 3

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Hynix Semiconductor Inc.

Kyungki-Do, Republic of Korea

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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Date 8/30/05

Typed or printed name James P. Zeller

Registration No. 28,491

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